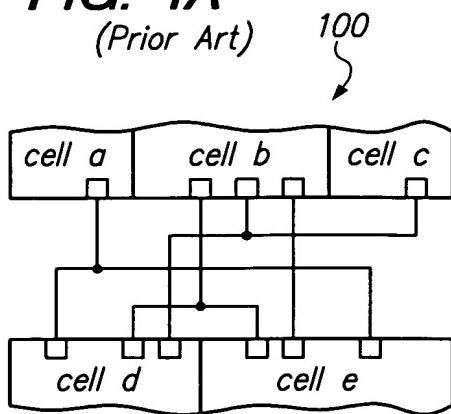


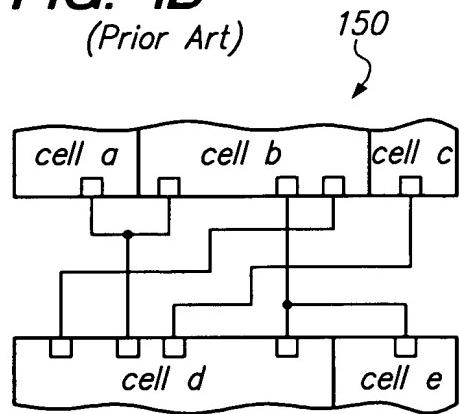
**FIG. 1A**

(Prior Art)



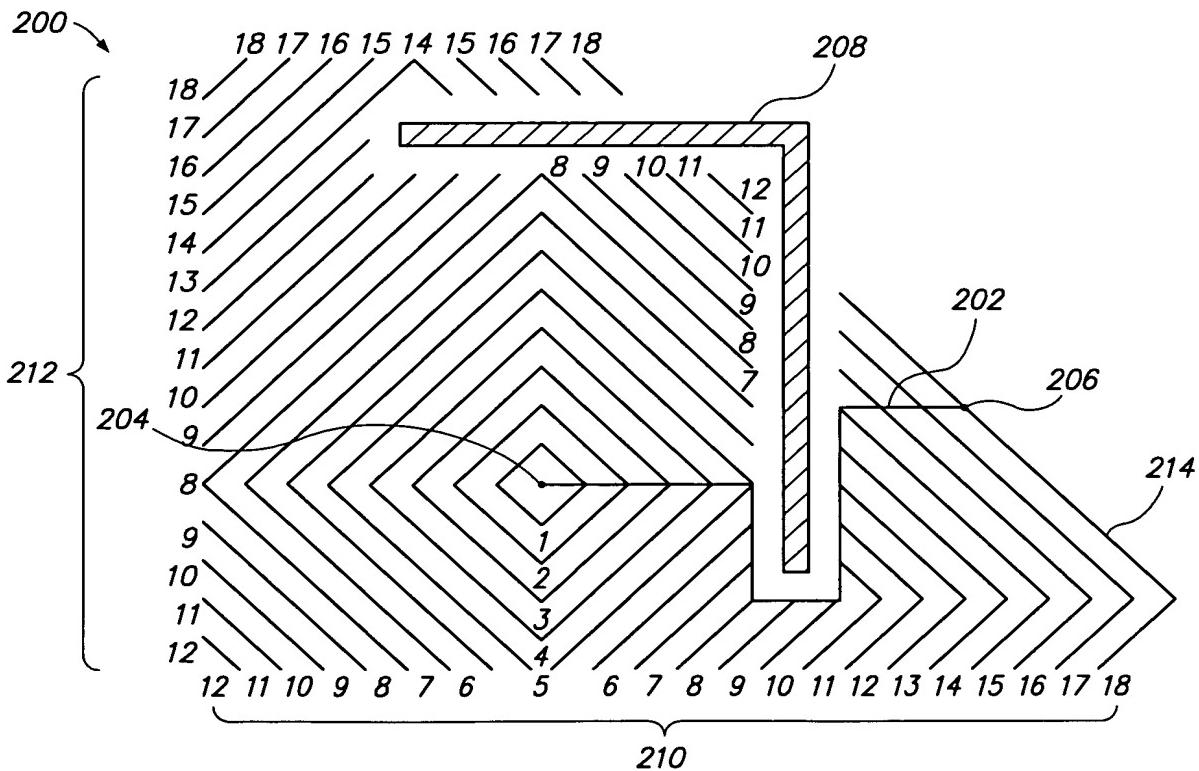
**FIG. 1B**

(Prior Art)



**FIG. 2**

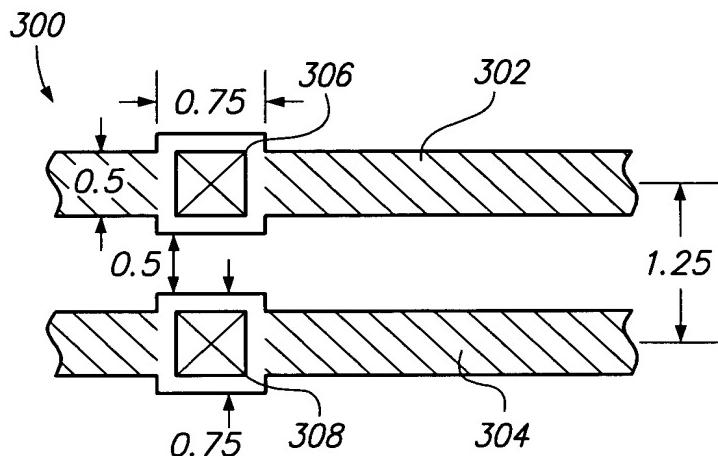
(Prior Art)



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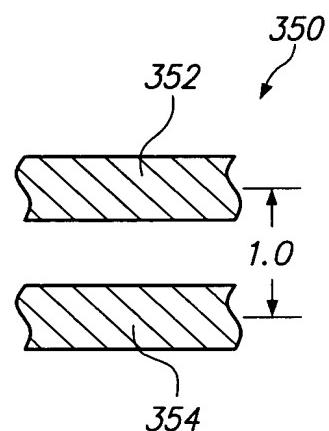
**FIG. 3A**

(Prior Art)

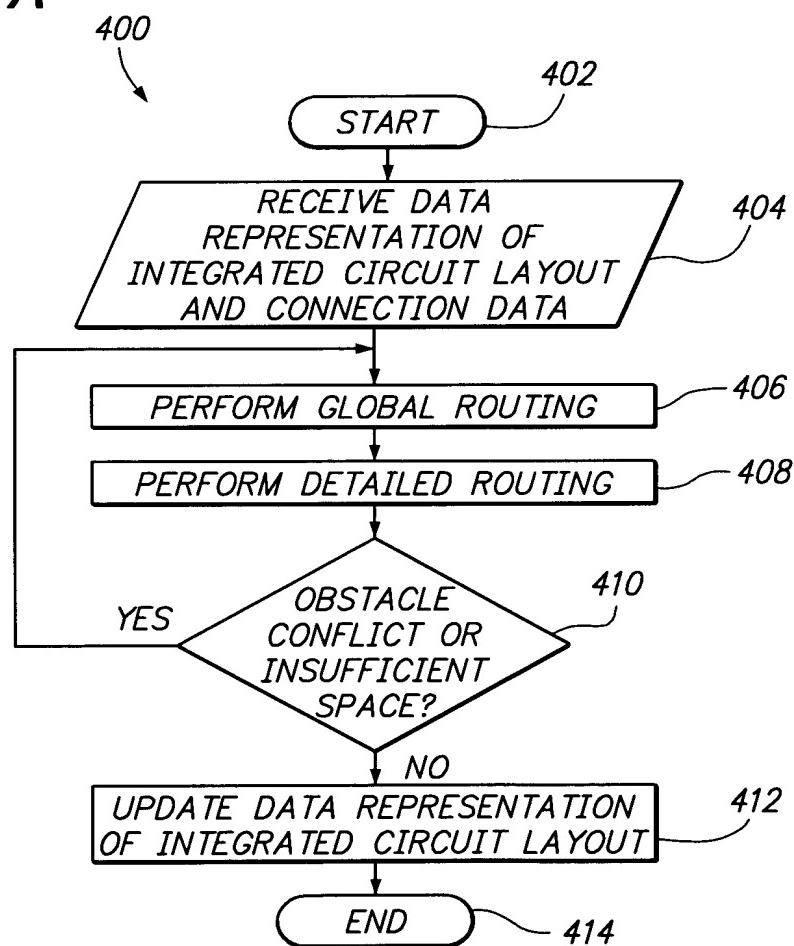


**FIG. 3B**

(Prior Art)



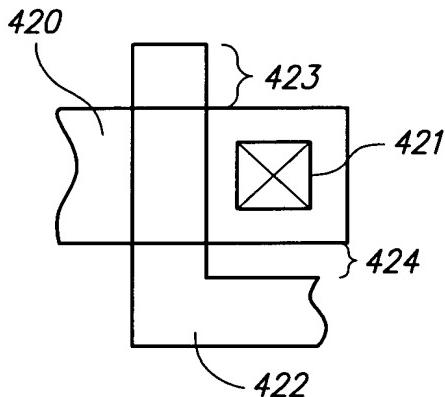
**FIG. 4A**



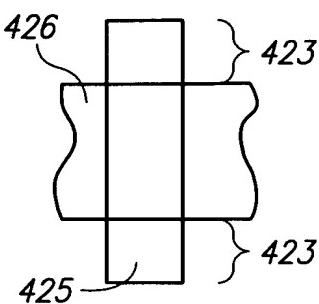
Entitled "Approach For Routing An Integrated Circuit"  
Application Serial No.: 09/421,437  
Filed: October 19, 1999  
Inventor: David C. Chapman  
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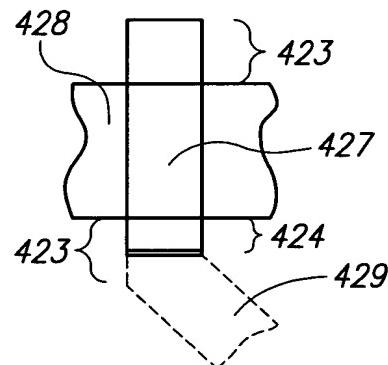
**FIG. 4B**



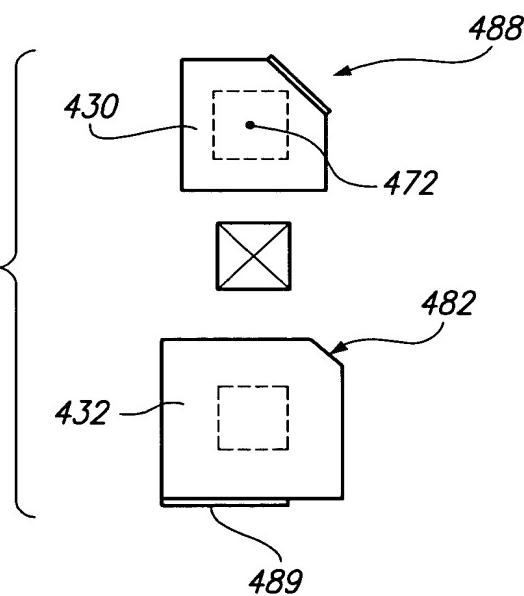
**FIG. 4C**



**FIG. 4D**

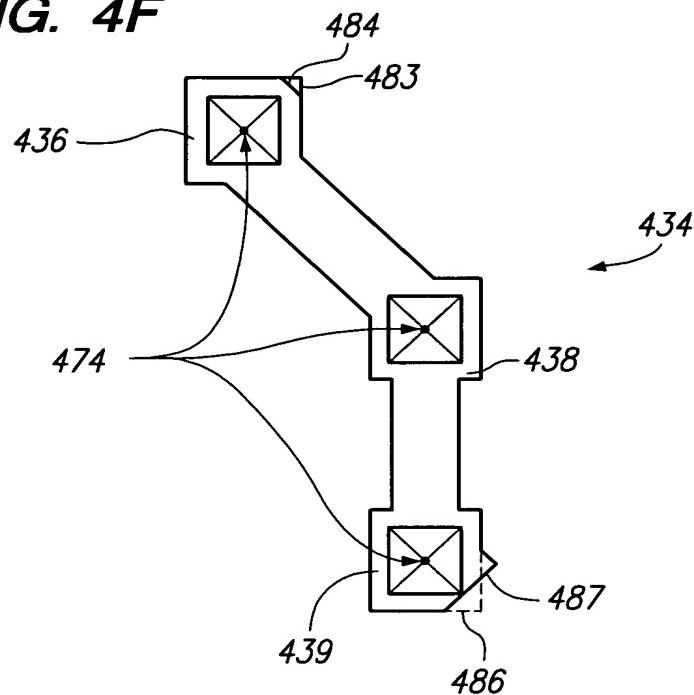


**FIG. 4E**

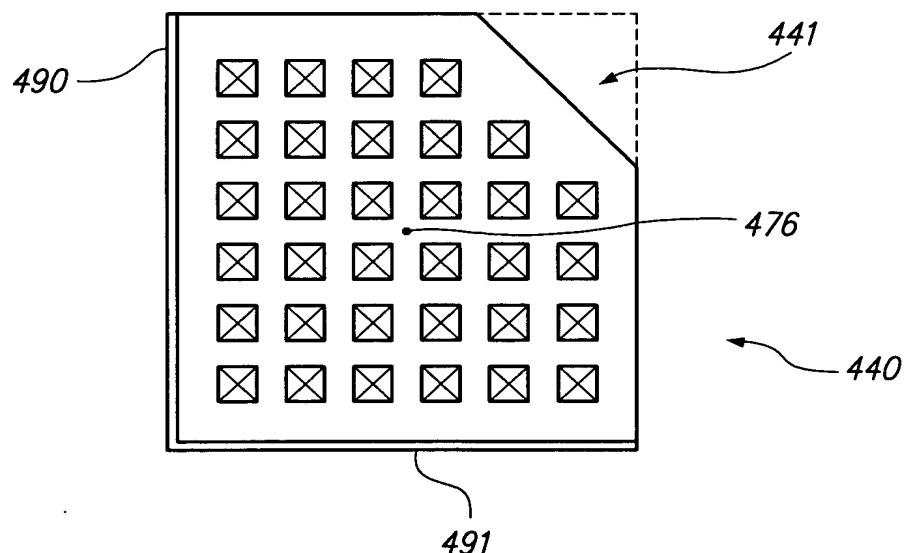


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**FIG. 4F**



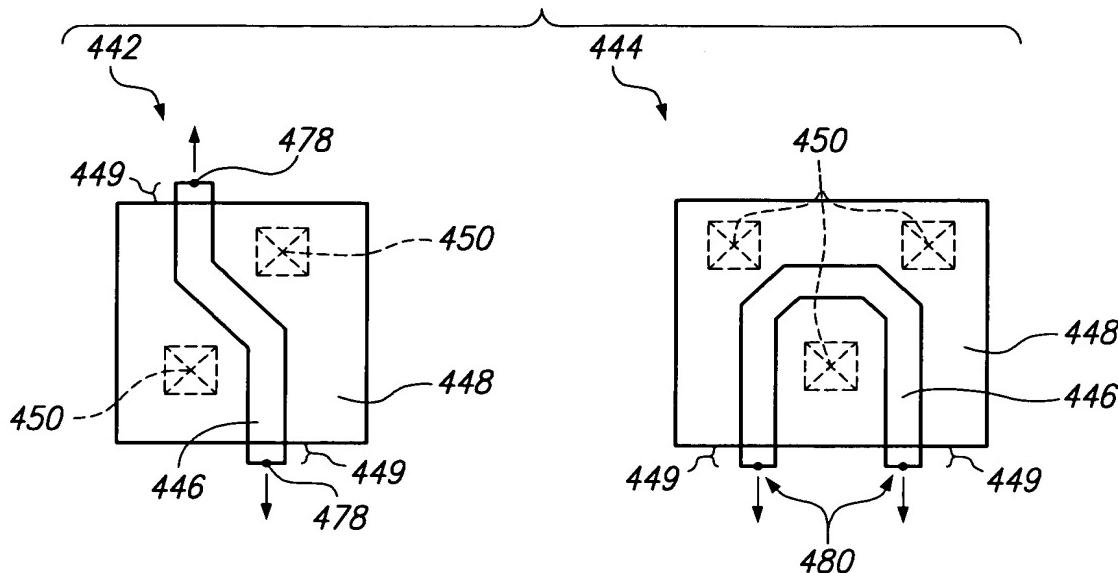
**FIG. 4G**



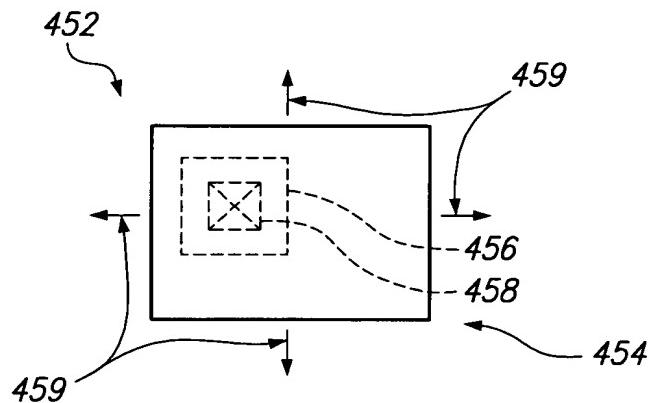
Entitled "Approach For Routing An Integrated Circuit"  
Application Serial No.: 09/421,437  
Filed: October 19, 1999  
Inventor: David C. Chapman  
Docket No. 50265-0018  
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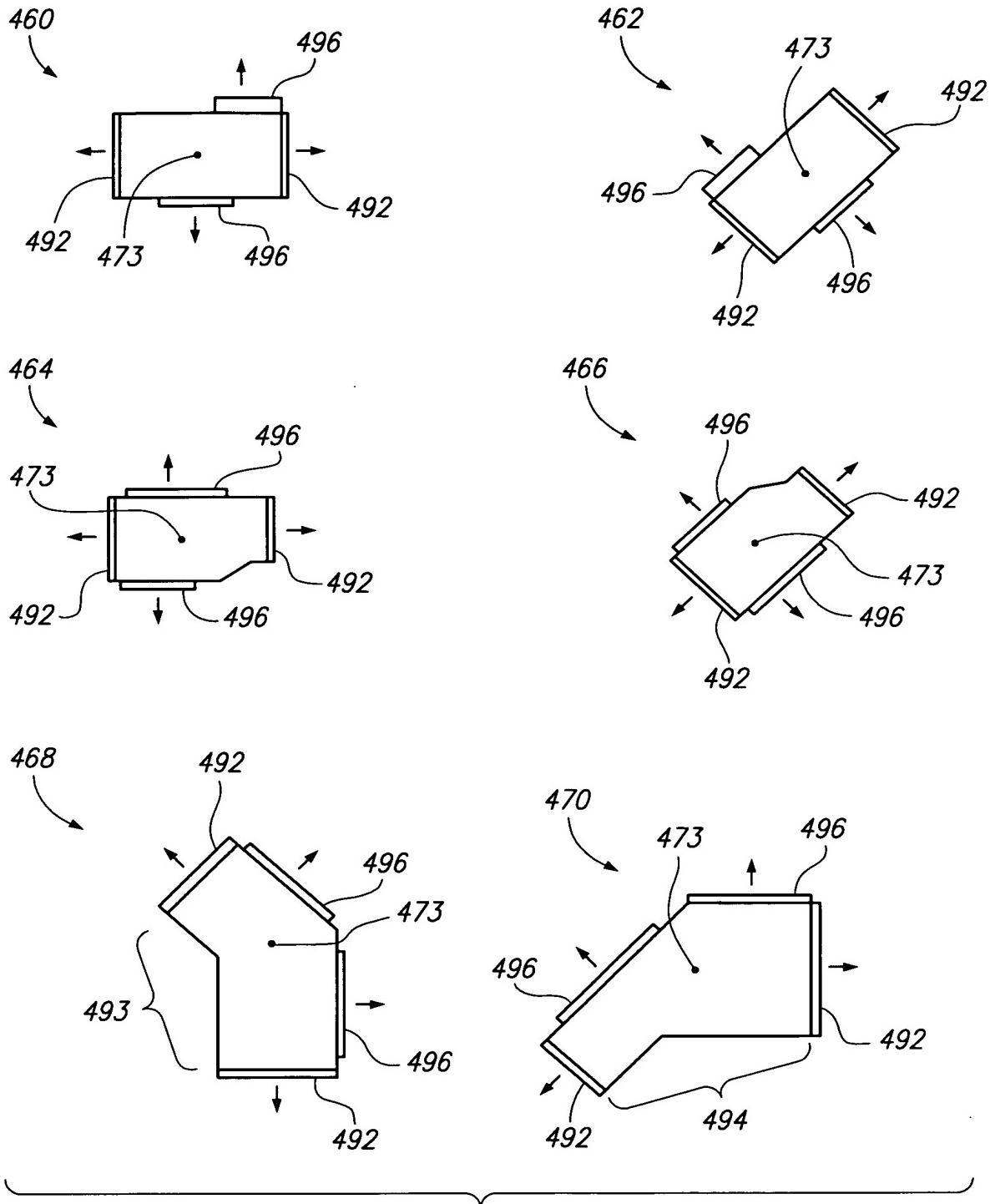
**FIG. 4H**



**FIG. 4I**



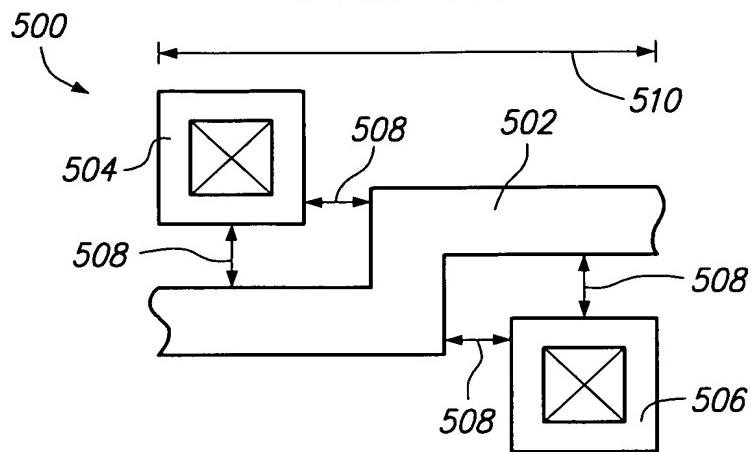
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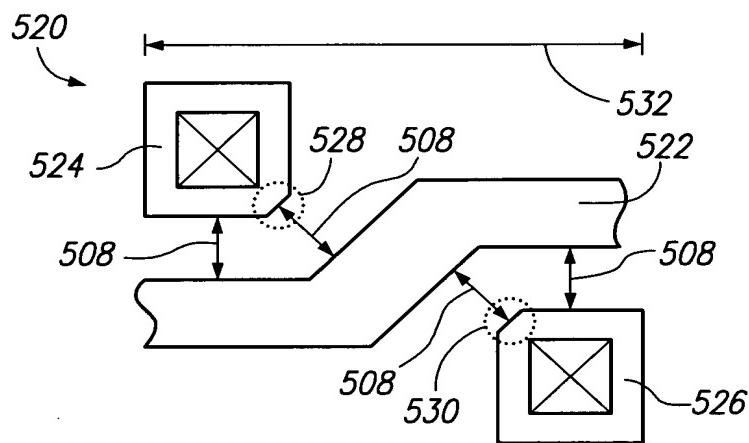
**FIG. 4J**

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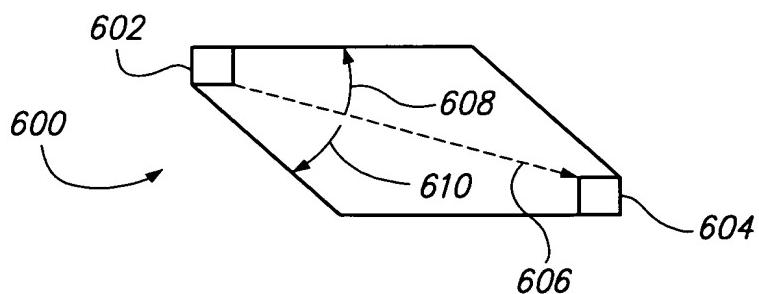
**FIG. 5A**



**FIG. 5B**

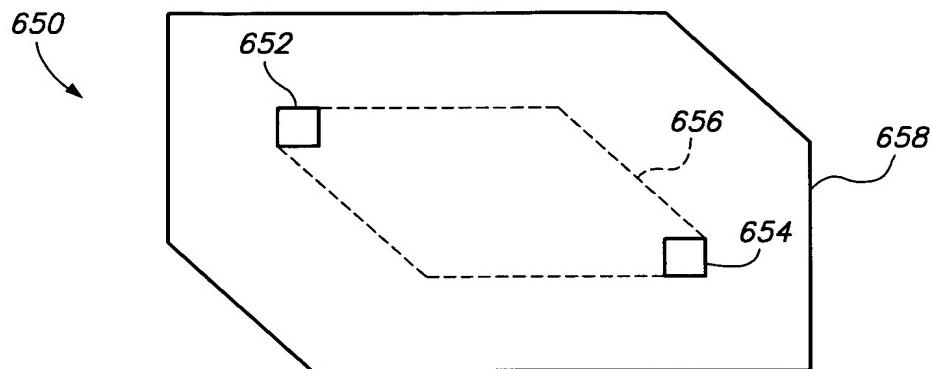


**FIG. 6A**

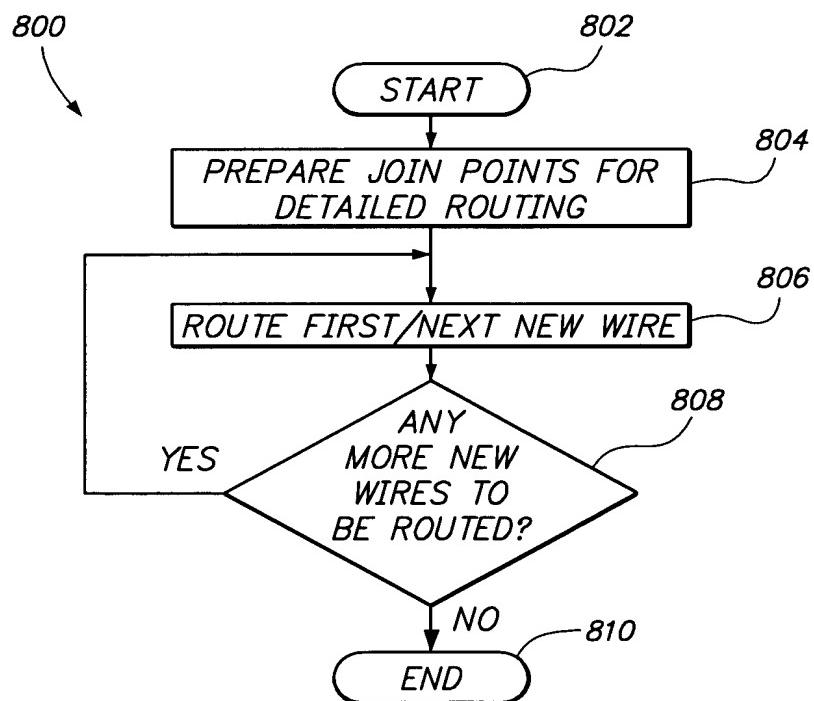


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**FIG. 6B**



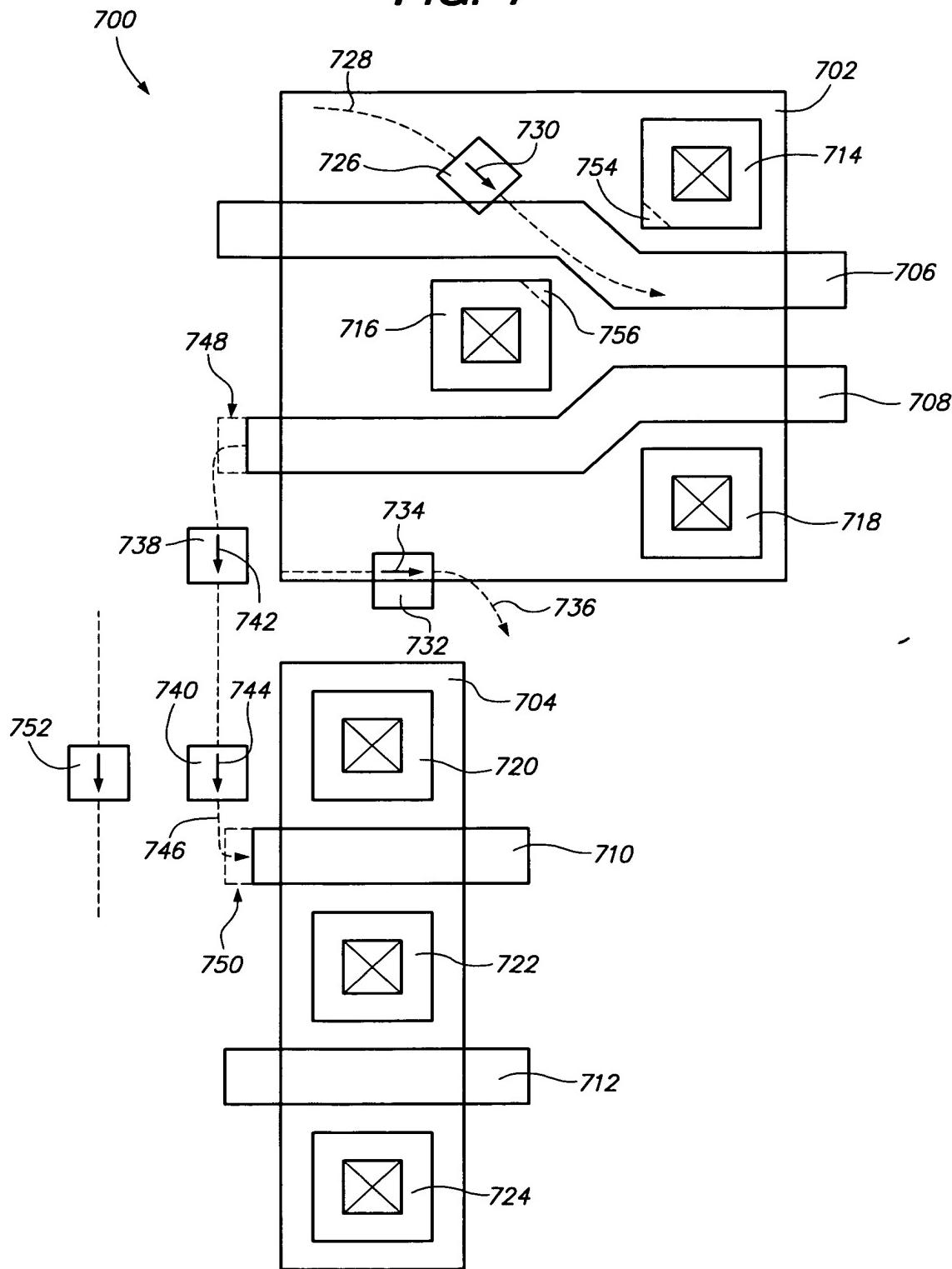
**FIG. 8**



Entitled Approach For Routing An Integrated Circuit  
Application Serial No.: 09/421,437  
Filed: October 19, 1999  
Inventor: David C. Chapman  
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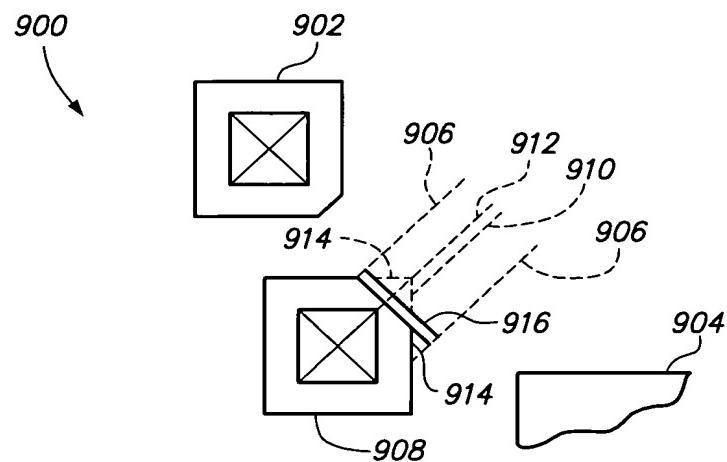
**FIG. 7**



Entitled "Approach For Routing An Integrated Circuit"  
Application Serial No.: 09/421,437  
Filed: October 19, 1999  
Inventor: David C. Chapman  
Docket No. 50265-0018  
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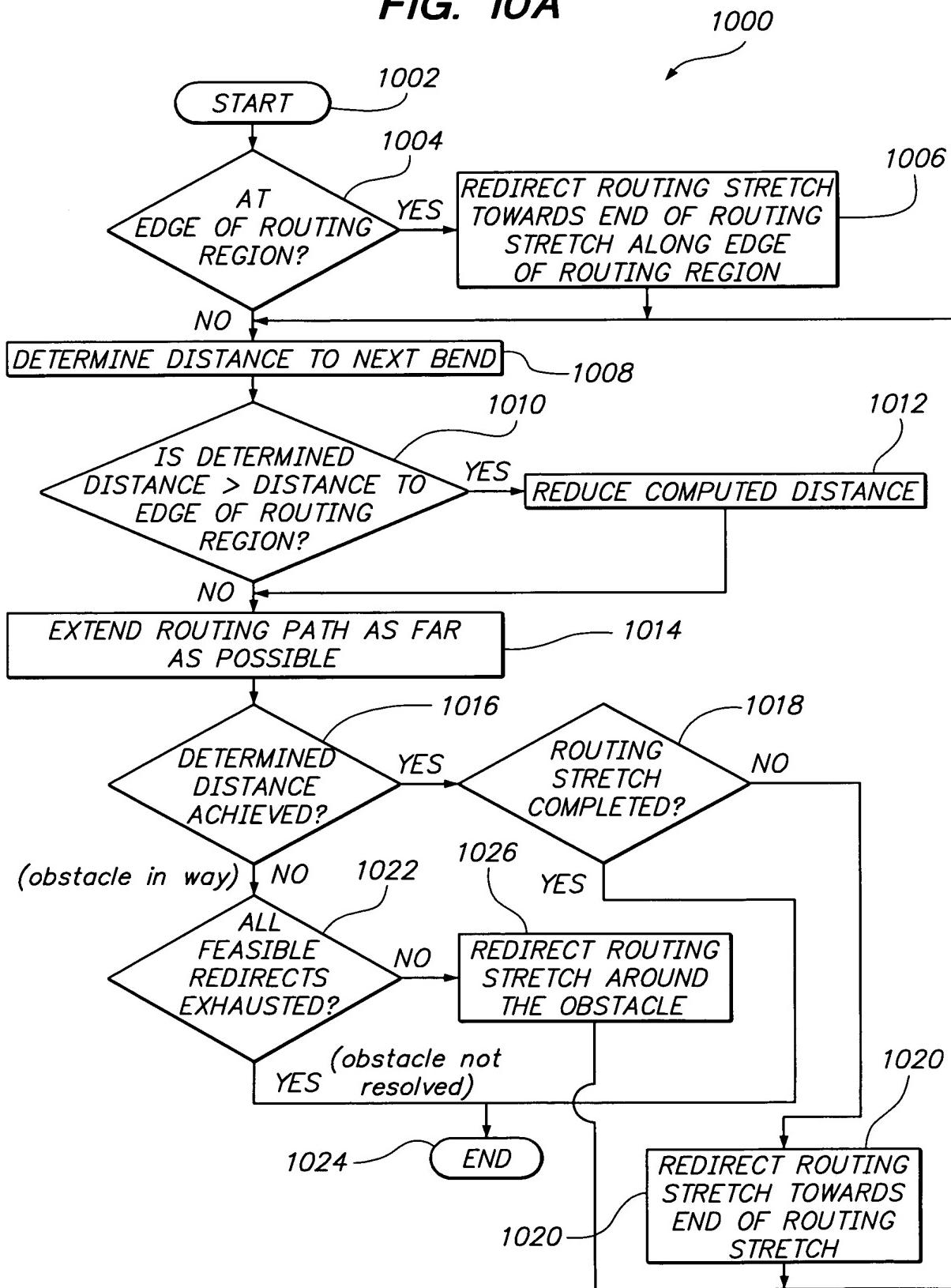
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**FIG. 9**



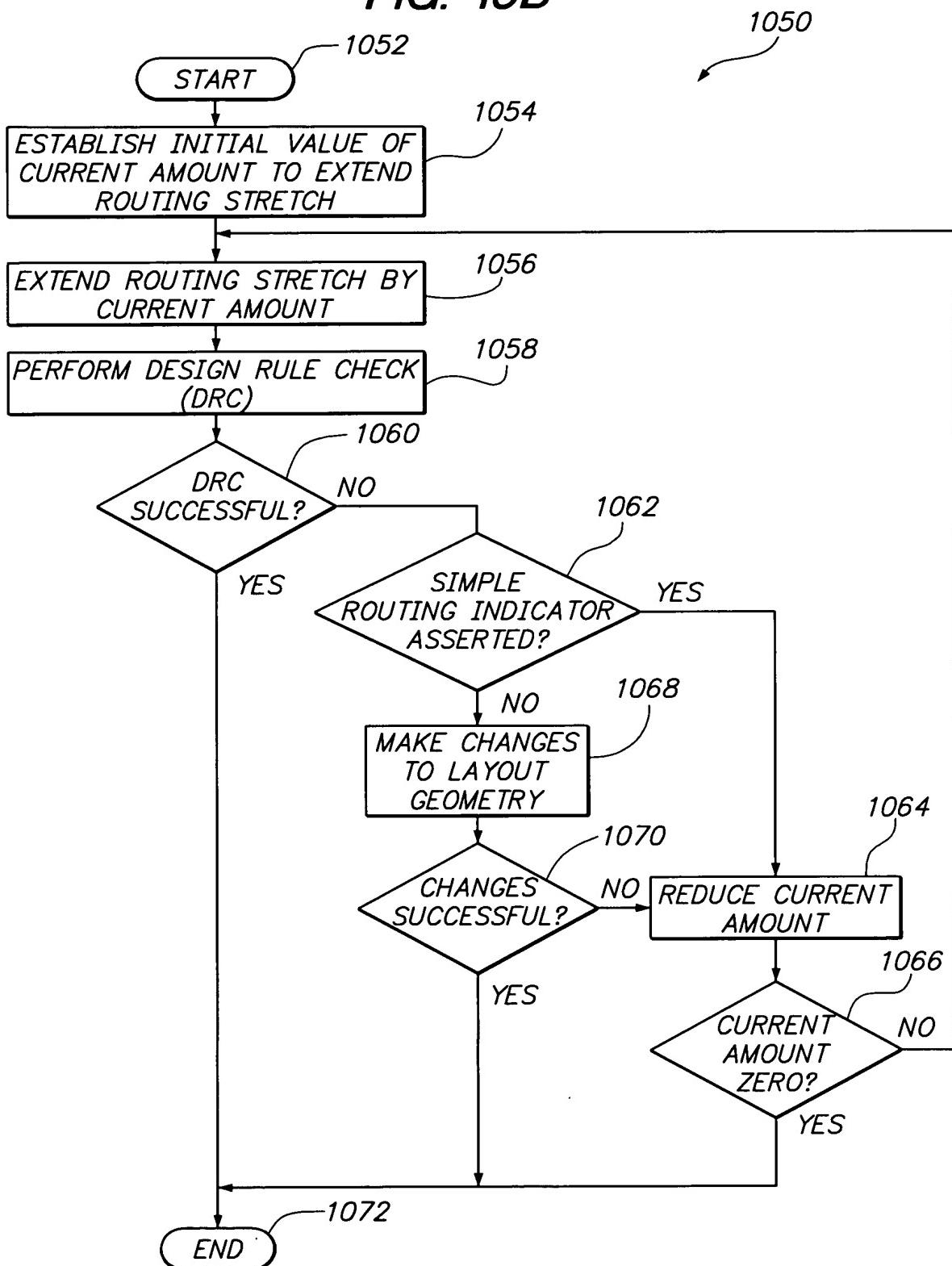
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**FIG. 10A**



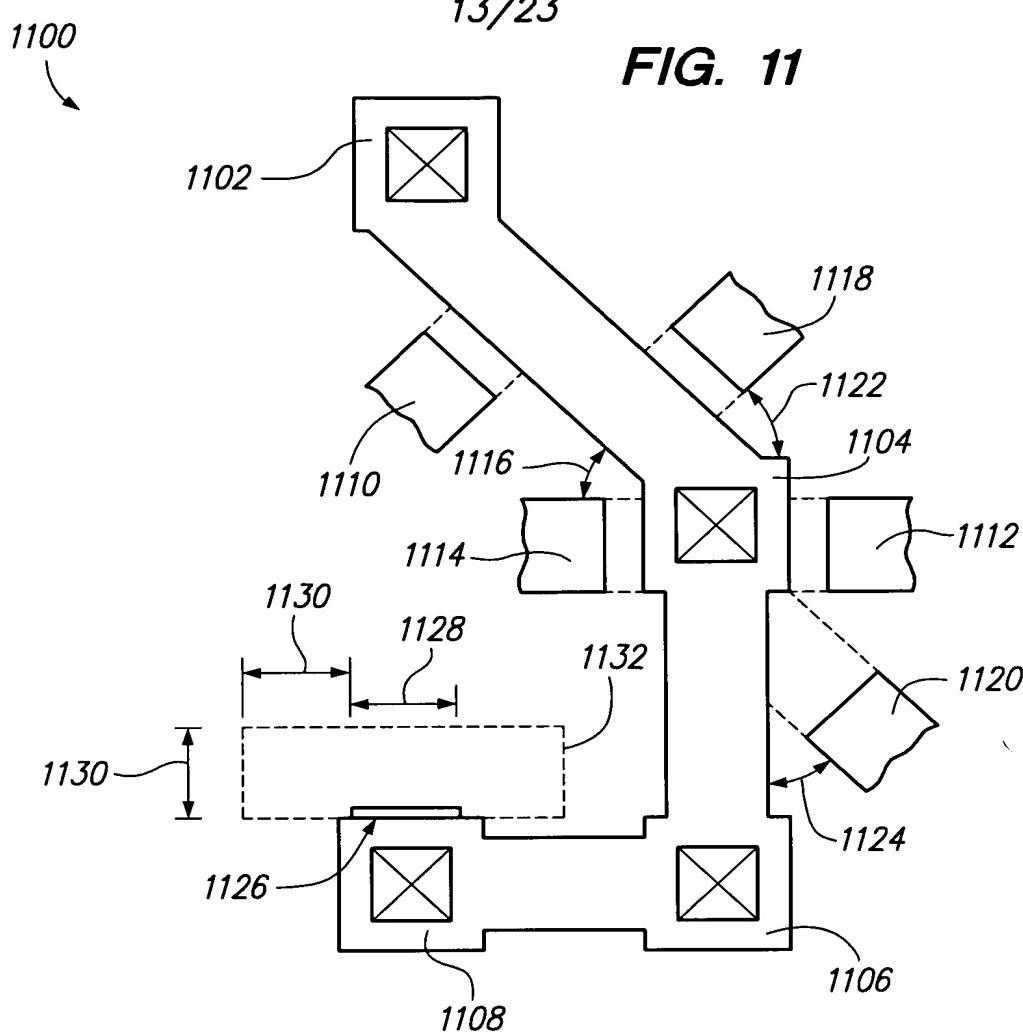
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**FIG. 10B**

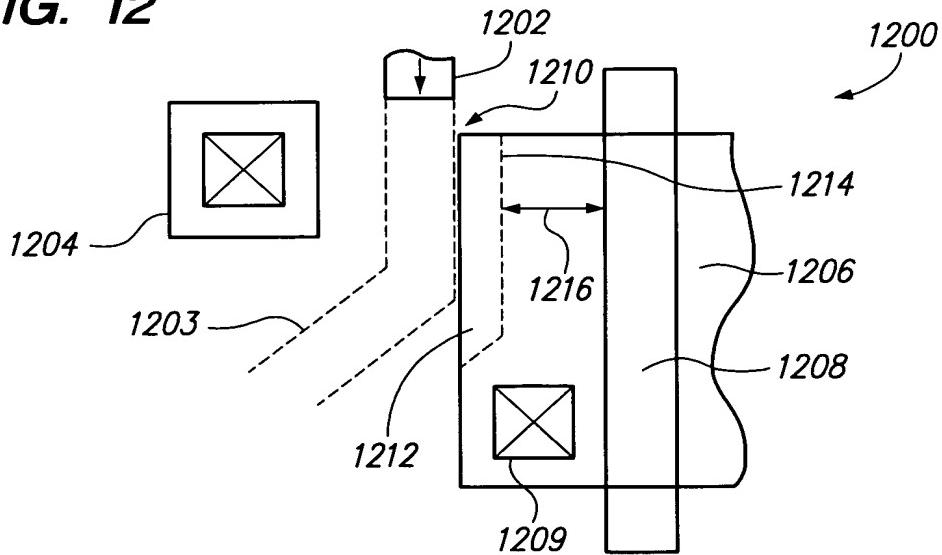


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**FIG. 11**

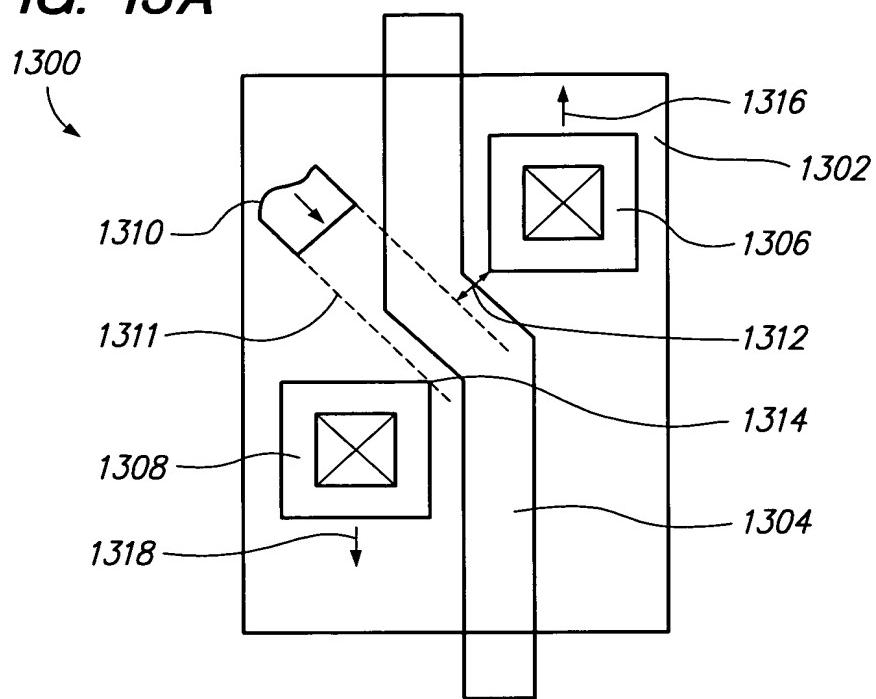


**FIG. 12**

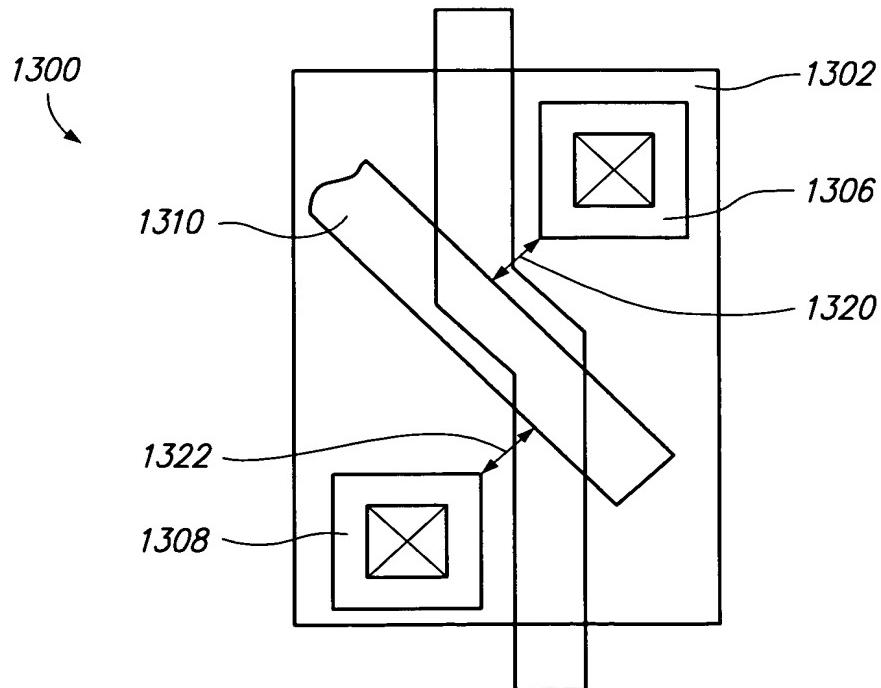


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**FIG. 13A**

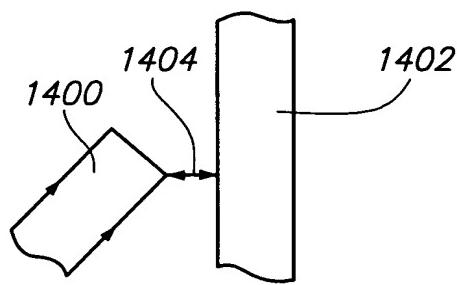


**FIG. 13B**

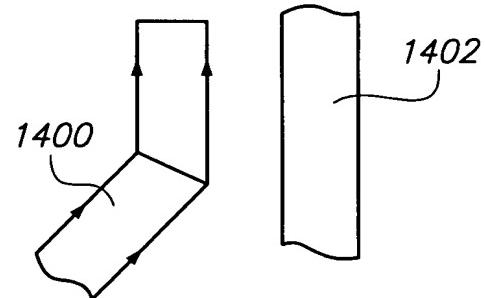


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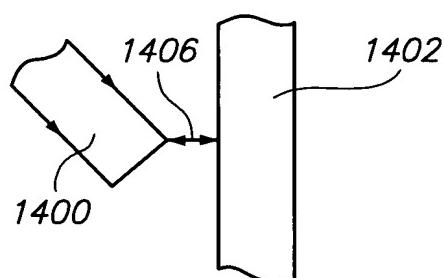
**FIG. 14A**



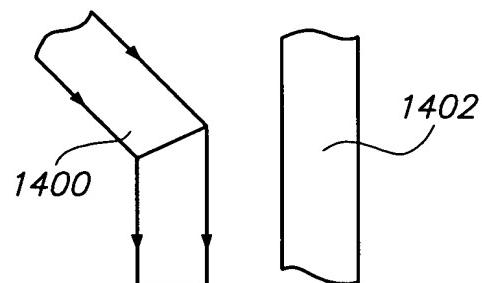
**FIG. 14B**



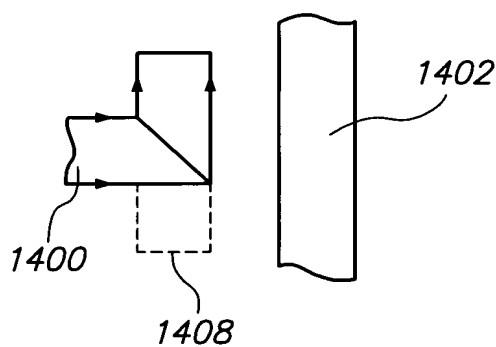
**FIG. 14C**



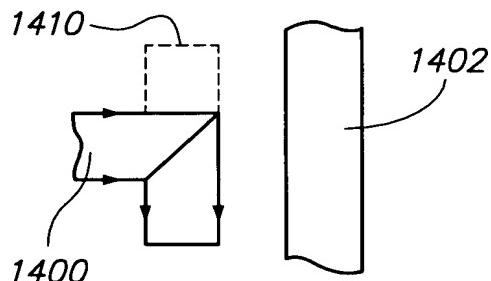
**FIG. 14D**



**FIG. 14E**

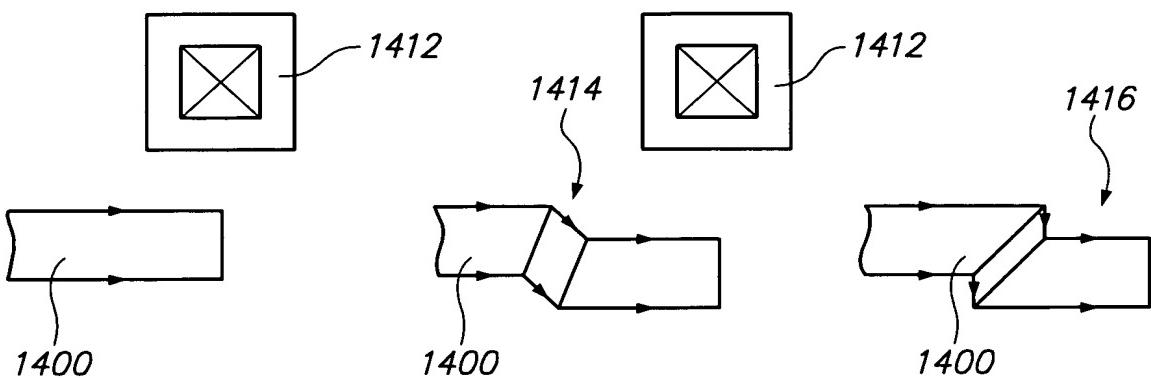


**FIG. 14F**

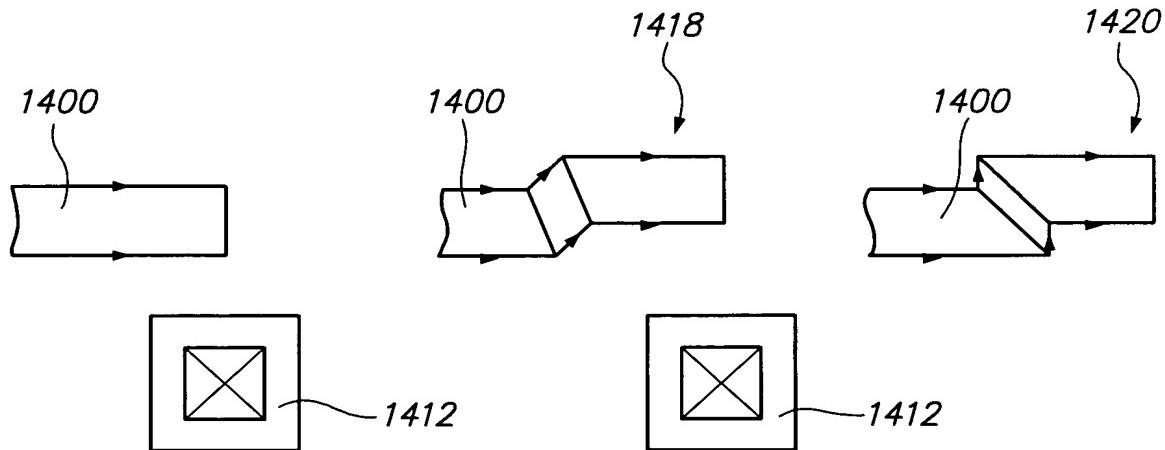


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**FIG. 14G**



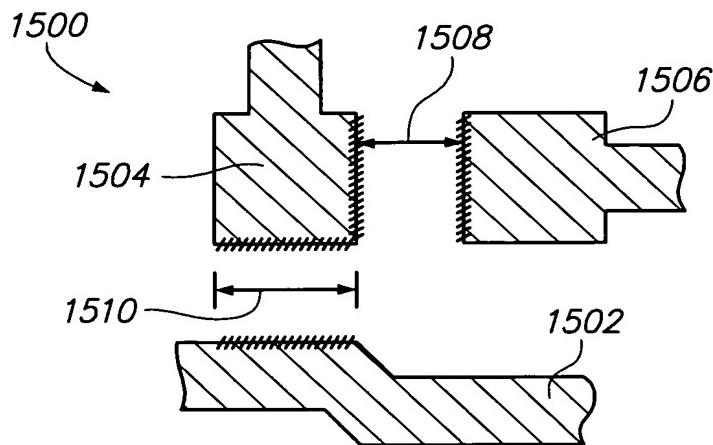
**FIG. 14H**



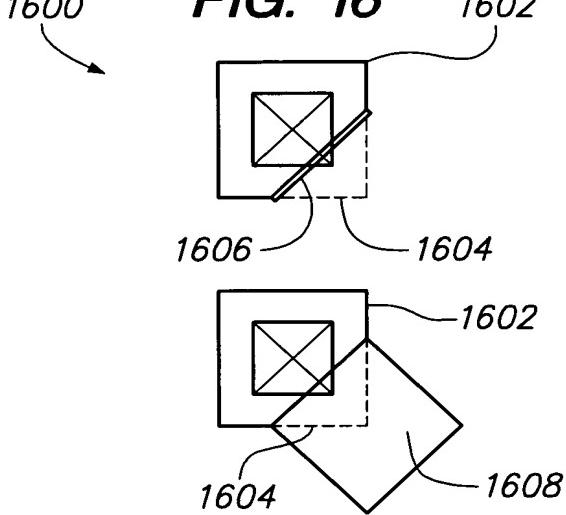
Entire Approach For Routing An Integrated Circuit  
Application Serial No.: 09/421,437  
Filed: October 19, 1999  
Inventor: David C. Chapman  
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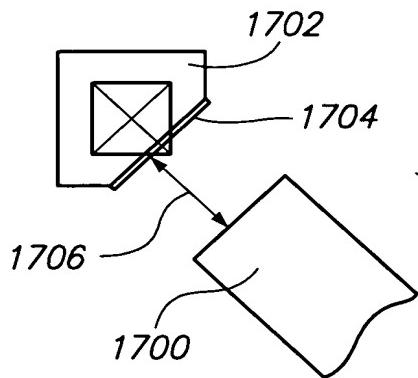
**FIG. 15**



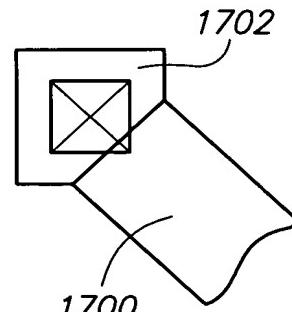
**FIG. 16**



**FIG. 17A**

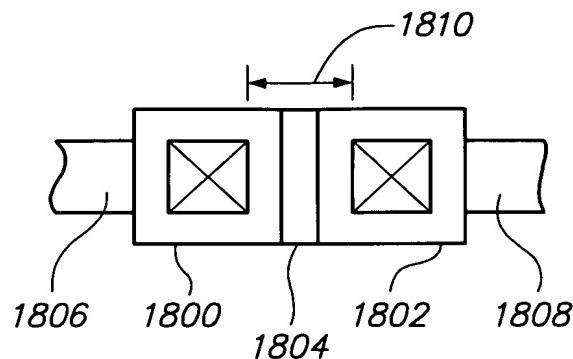


**FIG. 17B**

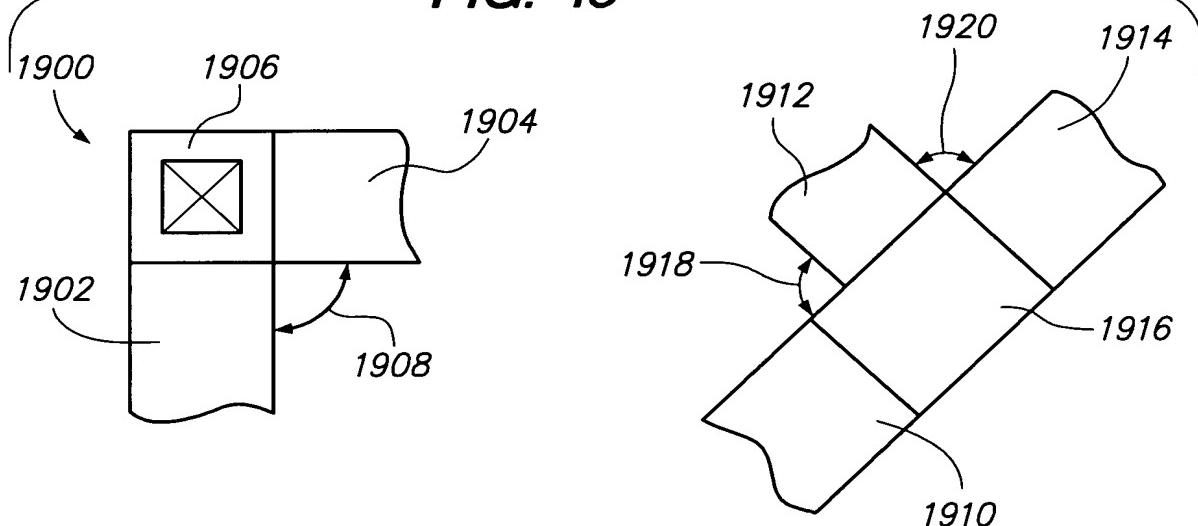


Entitled "Approach For Routing An Integrated Circuit" Application Serial No.: 09/421,437 Filed: October 19, 1999 Inventor: David C. Chapman Docket No. 50265-0018 REPLACEMENT SHEET

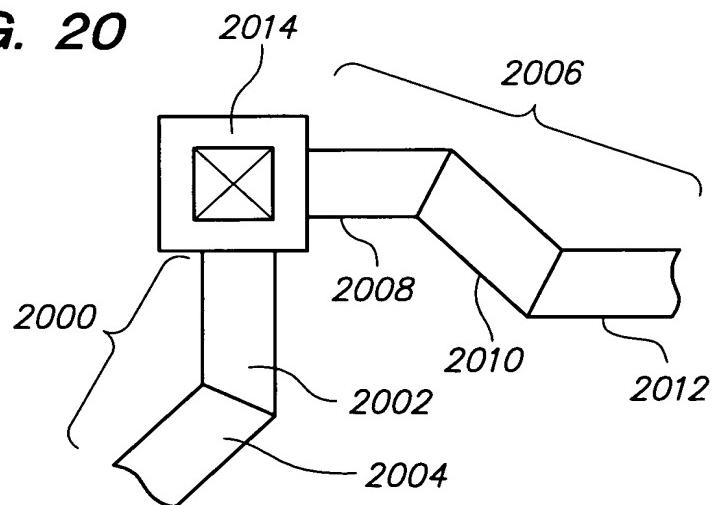
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**FIG. 18**



**FIG. 19**

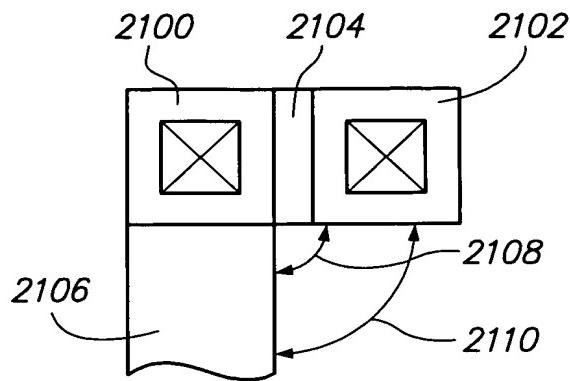


**FIG. 20**

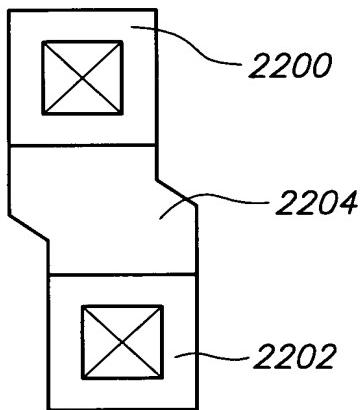


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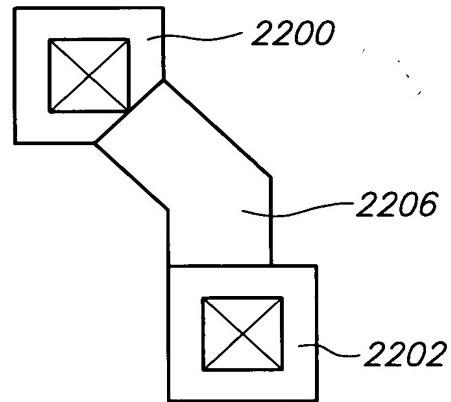
**FIG. 21**



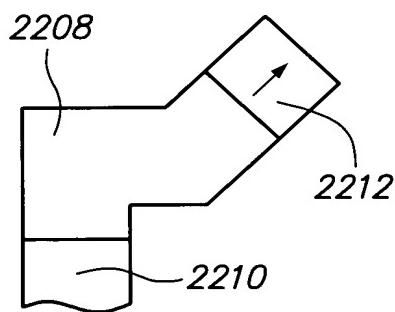
**FIG. 22A**



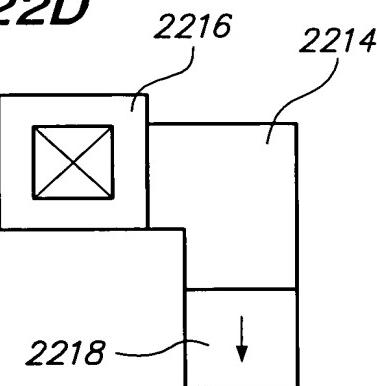
**FIG. 22B**



**FIG. 22C**

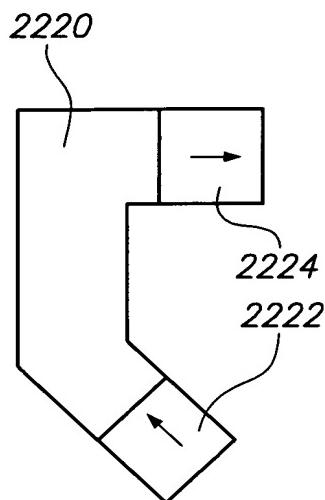


**FIG. 22D**

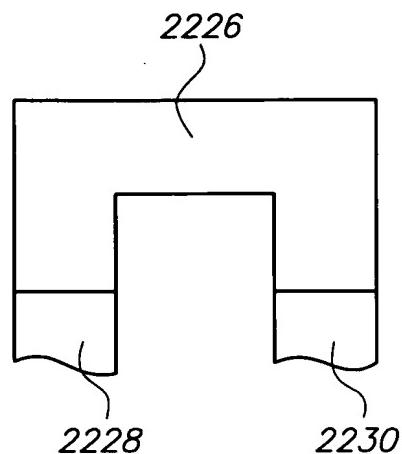


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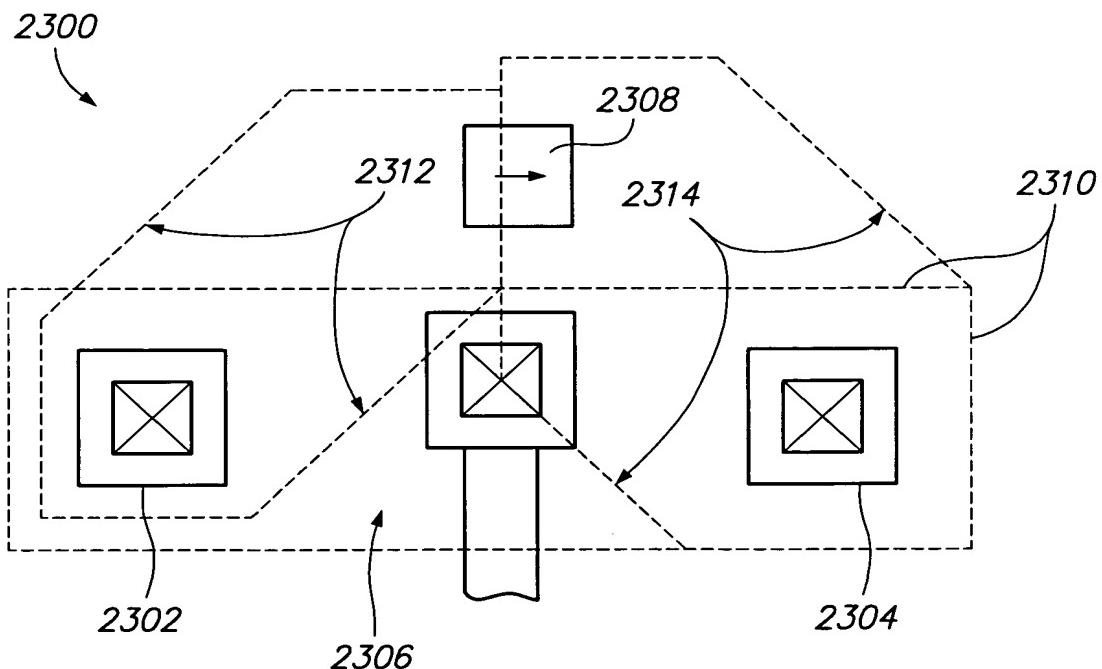
**FIG. 22E**



**FIG. 22F**

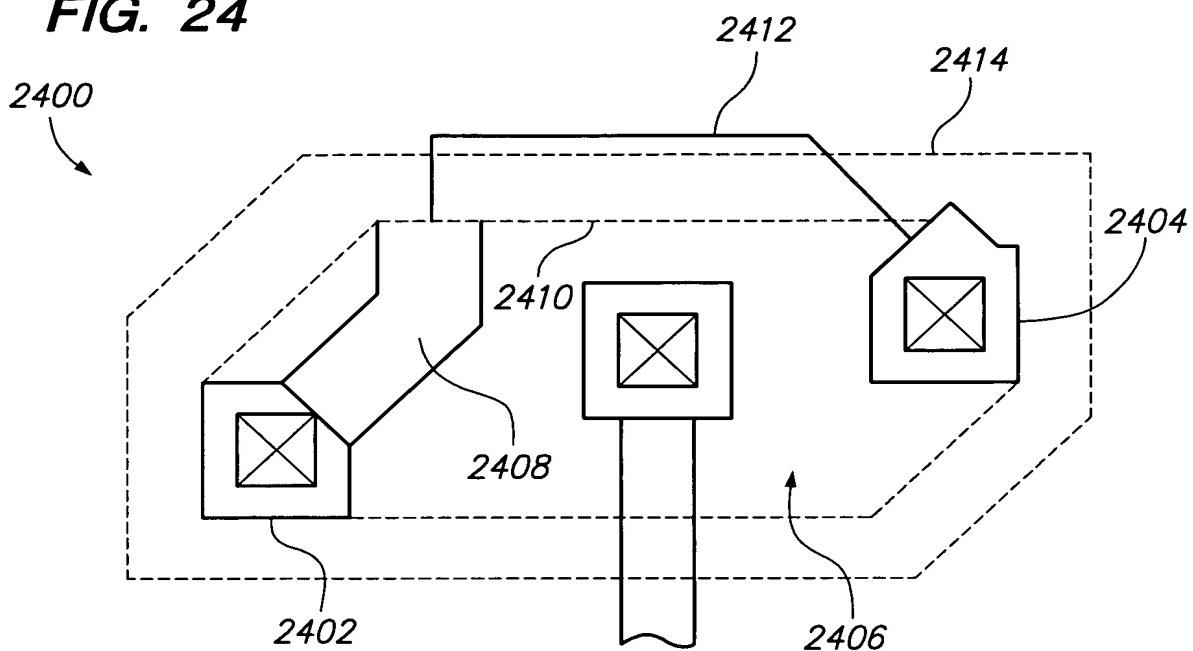


**FIG. 23**

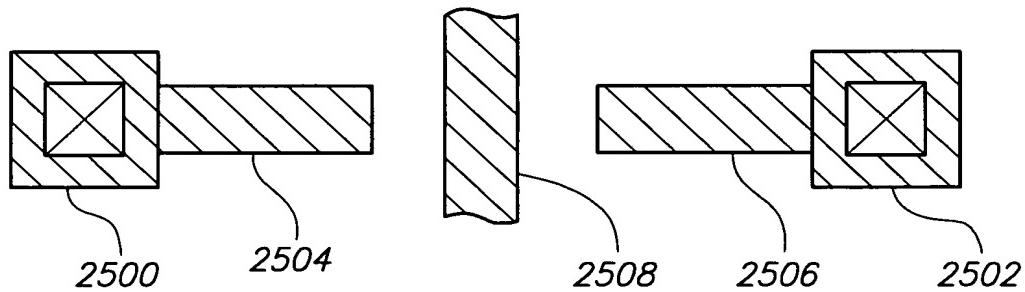


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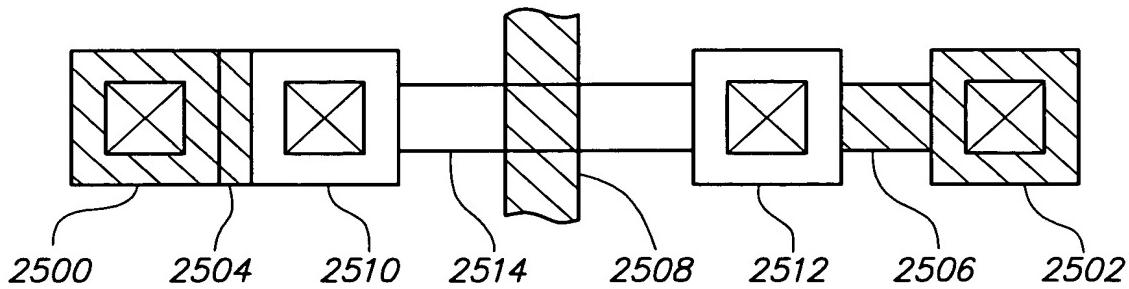
**FIG. 24**



**FIG. 25A**



**FIG. 25B**



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**FIG. 26**

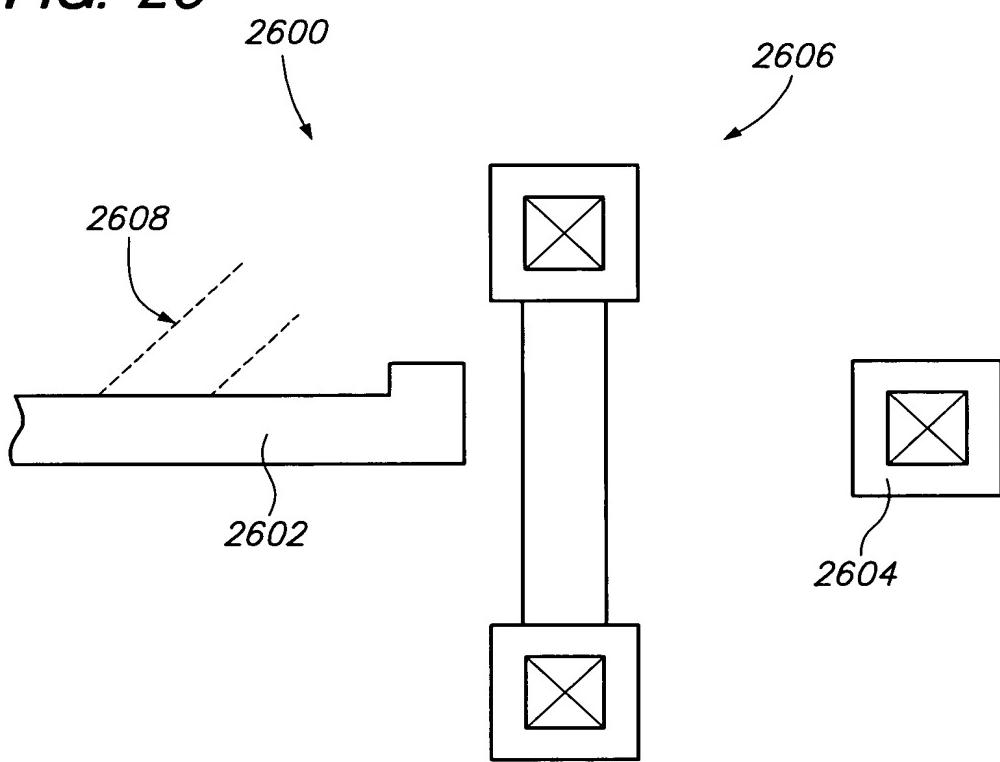


FIG. 27

